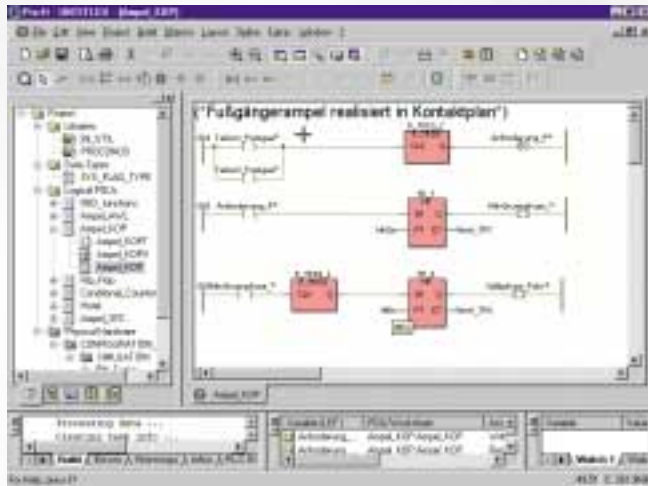


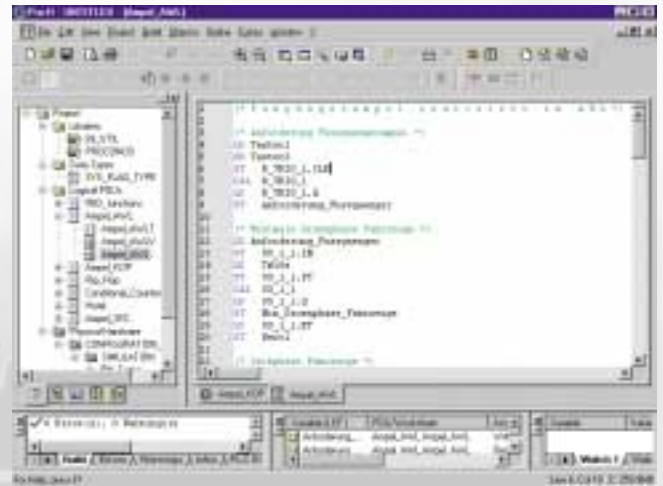


**Flexible choice of editors considerably decreases programming time**

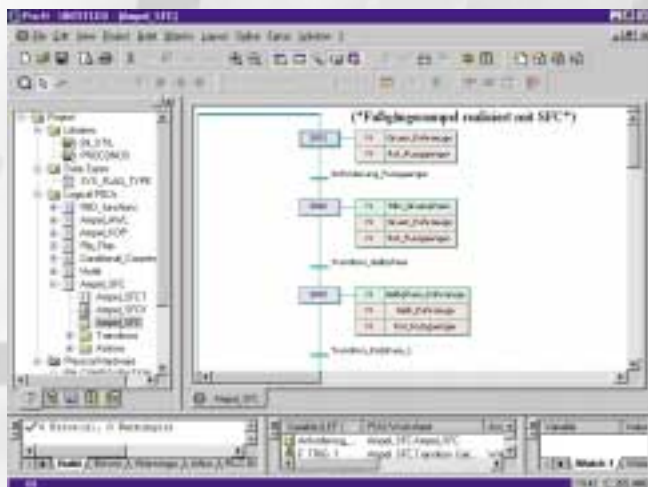
Ladder Editor (LD)



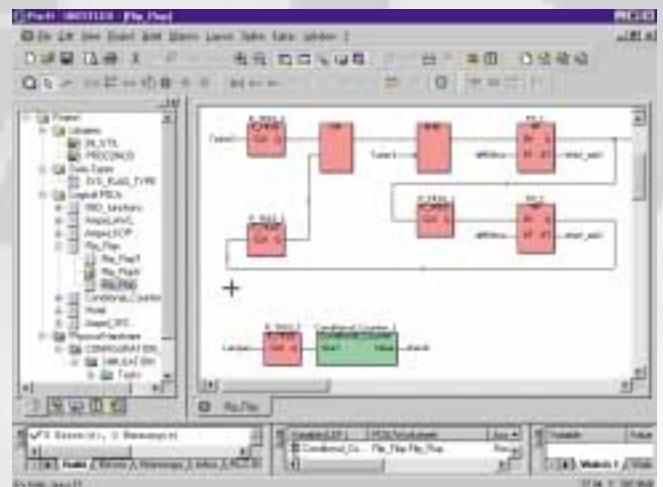
Instruction List (IL)



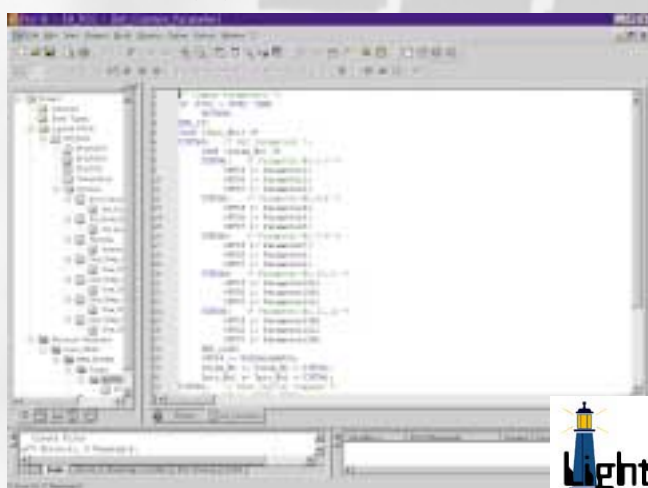
Sequential Function Chart (SFC)



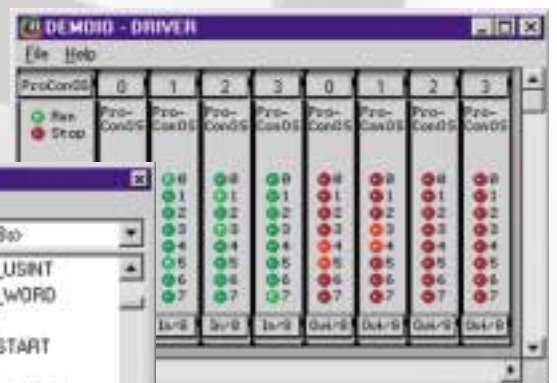
Function Block Diagram (FBD)



Structured Text (ST)



Function Block Library



Integrated virtual PLC allows program test without connection to target system



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